

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	263	(programmable adj logic) and (configuration adj memory) and compar\$3 and read\$3 and pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:13
S2	17	S1 and "713"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 15:21
S3	127	(configuration adj memory) and (configur\$3 near3 (programmable adj logic)) and compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 15:49
S4	44	S3 and (non adj volatile)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 15:53
S5	83	S3 not S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 15:53
S6	723	(326/40).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/28 09:53
S7	1305	(326/38).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/28 11:09
S8	1789	(713/1).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/28 10:09

S9	1005	(713/100).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/28 10:09
S10	815	(programmable adj logic) and (configuration adj memory) and (compar\$3 or match\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:26
S11	23	S10 and 713/1,100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:46
S12	12	(programmable adj logic) and (nonvolatile adj configuration adj memory) and (compar\$3 or match\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:42
S13	0	((programmable adj logic) and (nonvolatile adj configuration adj memory) and (compar\$3 or match\$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:42
S14	0	((programmable adj logic) and (nonvolatile adj configuration adj memory) and (compar\$3 or match\$3)).clm.	US-PGPUB	OR	ON	2005/10/28 10:44
S15	0	((programmable adj logic) and (nonvolatile adj configuration adj memory)).clm.	US-PGPUB	OR	ON	2005/10/28 10:43
S16	1	((programmable adj logic) and (nonvolatile adj configuration adj memory)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:42
S17	0	((PLA or PLD) and (nonvolatile adj configuration adj memory) and (compar\$3 or match\$3)).clm.	US-PGPUB	OR	ON	2005/10/28 10:45
S18	11	((PLA or PLD) and (nonvolatile adj configuration adj memory) and (compar\$3 or match\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:45

S19	578	((PLA or PLD) and (configuration adj memory) and (compar\$3 or match\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:45
S20	14	S19 and 713/1,2,100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:08
S21	13	S20 not S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:46
S22	13	S21 not S18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:47
S23	1255	(326/39).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/28 11:48
S24	903	S10 or S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 11:51
S25	256	S24 and ("2 bit" or (two adj bit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 11:51
S26	562	S24 and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 11:51

S27	211	S25 and S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 11:52
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